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**TRANSMITTAL
FORM**

(to be used for all correspondence after initial filing)

Application Number	10/084,803
Filing Date	02/28/2002
First Named Inventor	Charles Douglas Murphy
Art Unit	2819
Examiner Name	Jean Bruner Jeangle
Attorney Docket Number	

Total Number of Pages in This Submission

ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance communication to Group
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<input type="checkbox"/> Response to Missing Parts/Incomplete Application	Remarks	
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Charles Douglas Murphy
Signature	Charles Douglas Murphy
Date	5/5/2003

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.

Typed or printed name	Charles Douglas Murphy		
Signature	Charles Douglas Murphy	Date	5/5/2003

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5-21-03

Application Number: 10/084,803
Filing Date: 02/28/2002
Applicant: Charles Douglas Murphy
Title: SHARED PARALLÉL DIGITAL-TO-ANALOG CONVERSION
Examiner/GAU: Jean Bruner Jeanglaude

May 2, 2003

AMENDMENT A

Mail Stop Non-Fee Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed April 21, 2003, please amend the above application as follows:

Specification:

p. 27 - Replace the Abstract with the following paragraph:

A digital-to-analog converter in which a counter tracks the elapsed time since the beginning of a conversion cycle. Simultaneously, a reference analog signal such as a voltage ramp is generated. When the count reaches a stored digital number value, a sample-and-hold circuit acquires the value of the reference analog signal. Multiple stored digital number values can be converted using multiple sample-and-hold circuits which share the counter and the reference analog signal. Such parallel digital-to-analog conversion is useful in many applications such as digital communications, image display, and shared parallel analog-to-digital conversion.

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